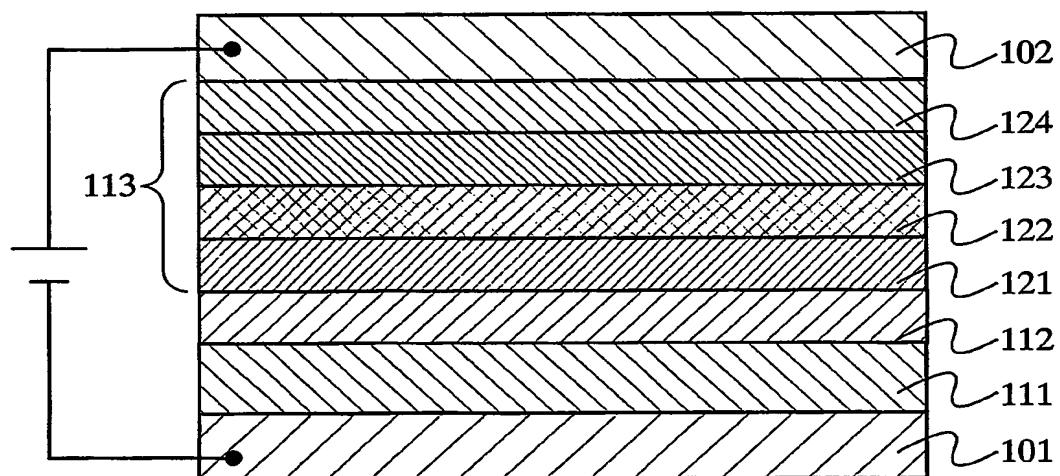


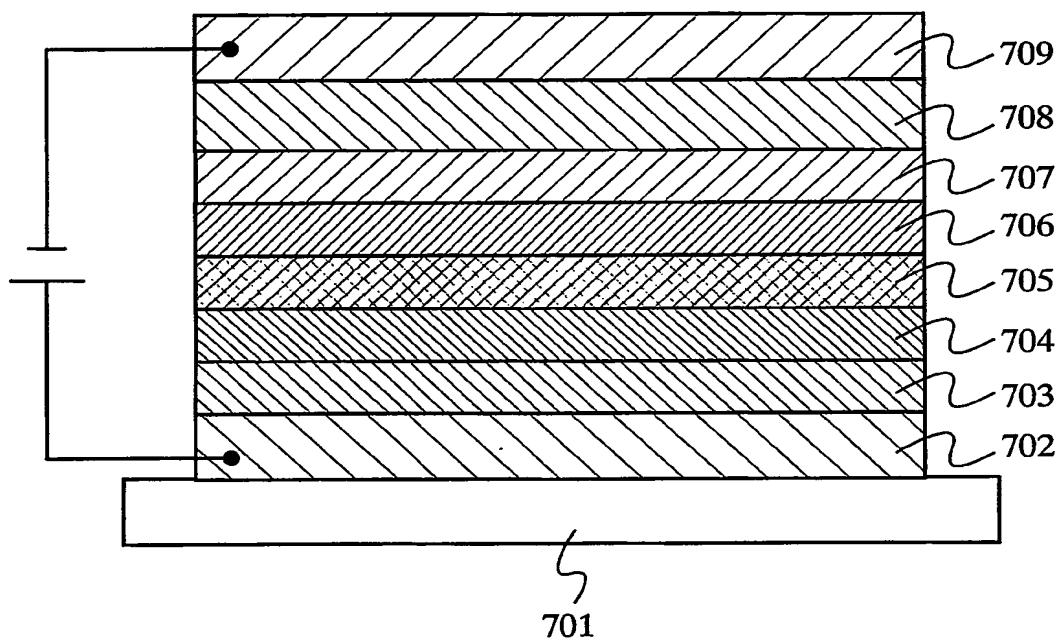
1/28

FIG. 1



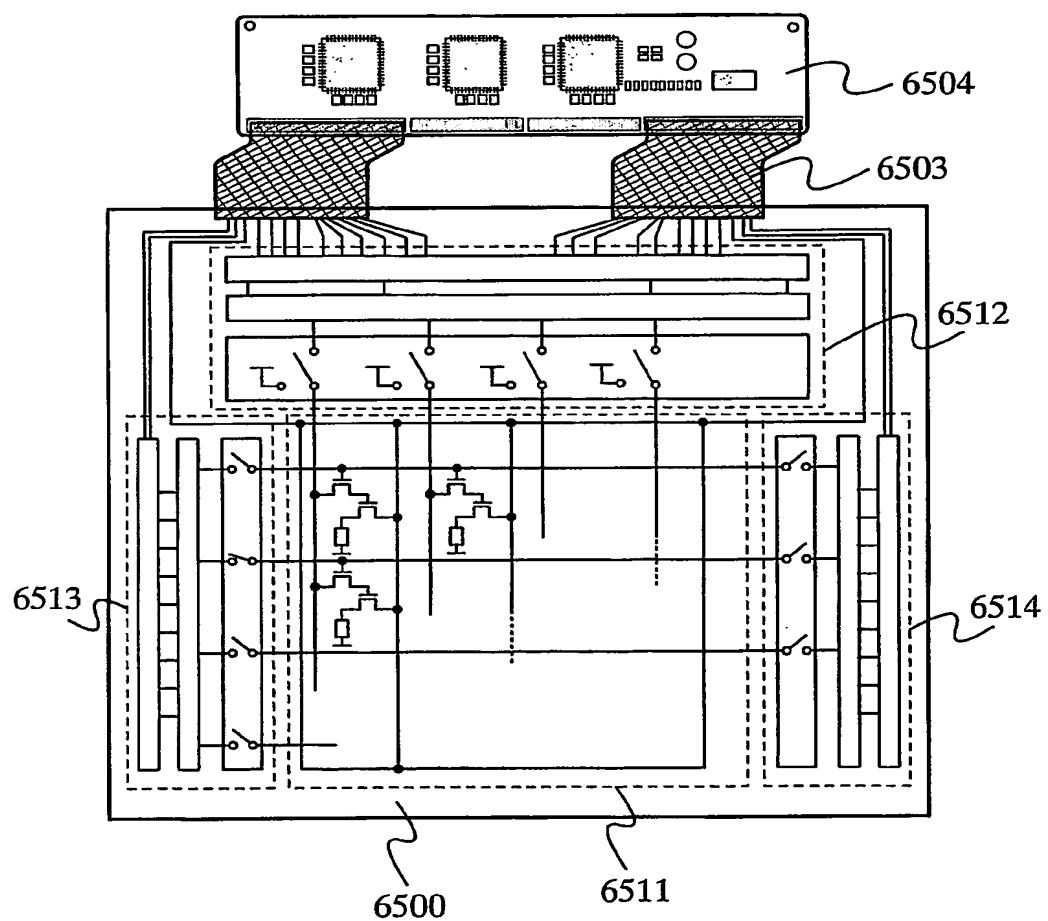
2/28

FIG. 2



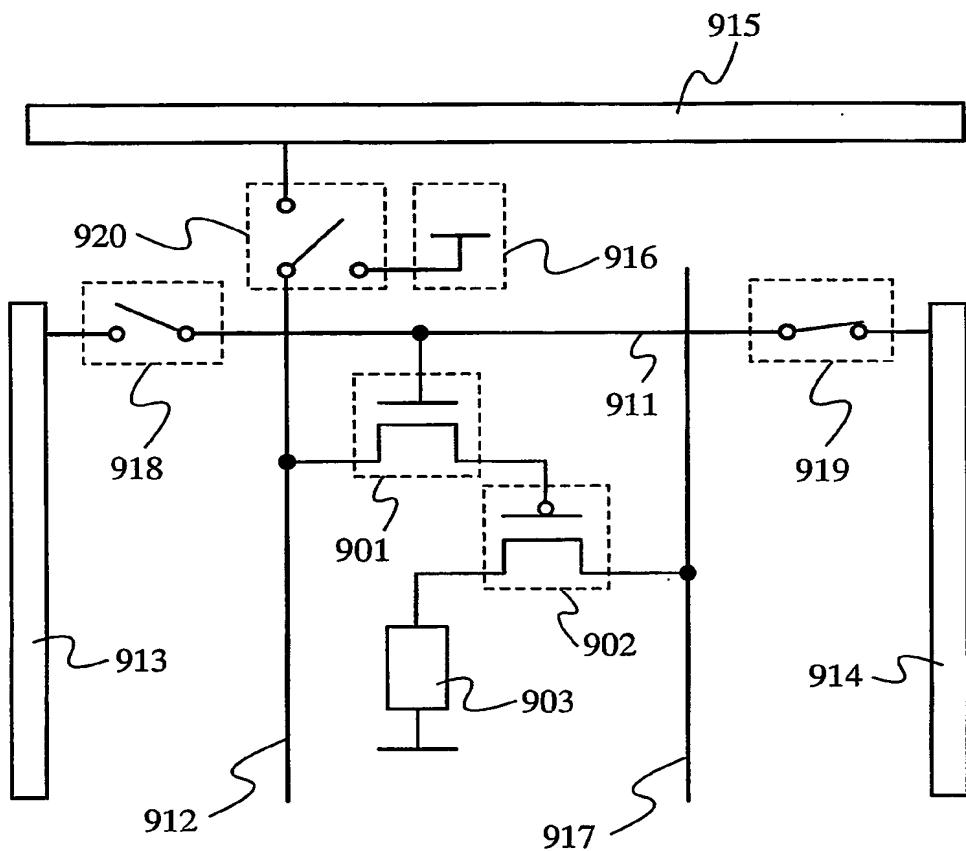
3/28

FIG. 3



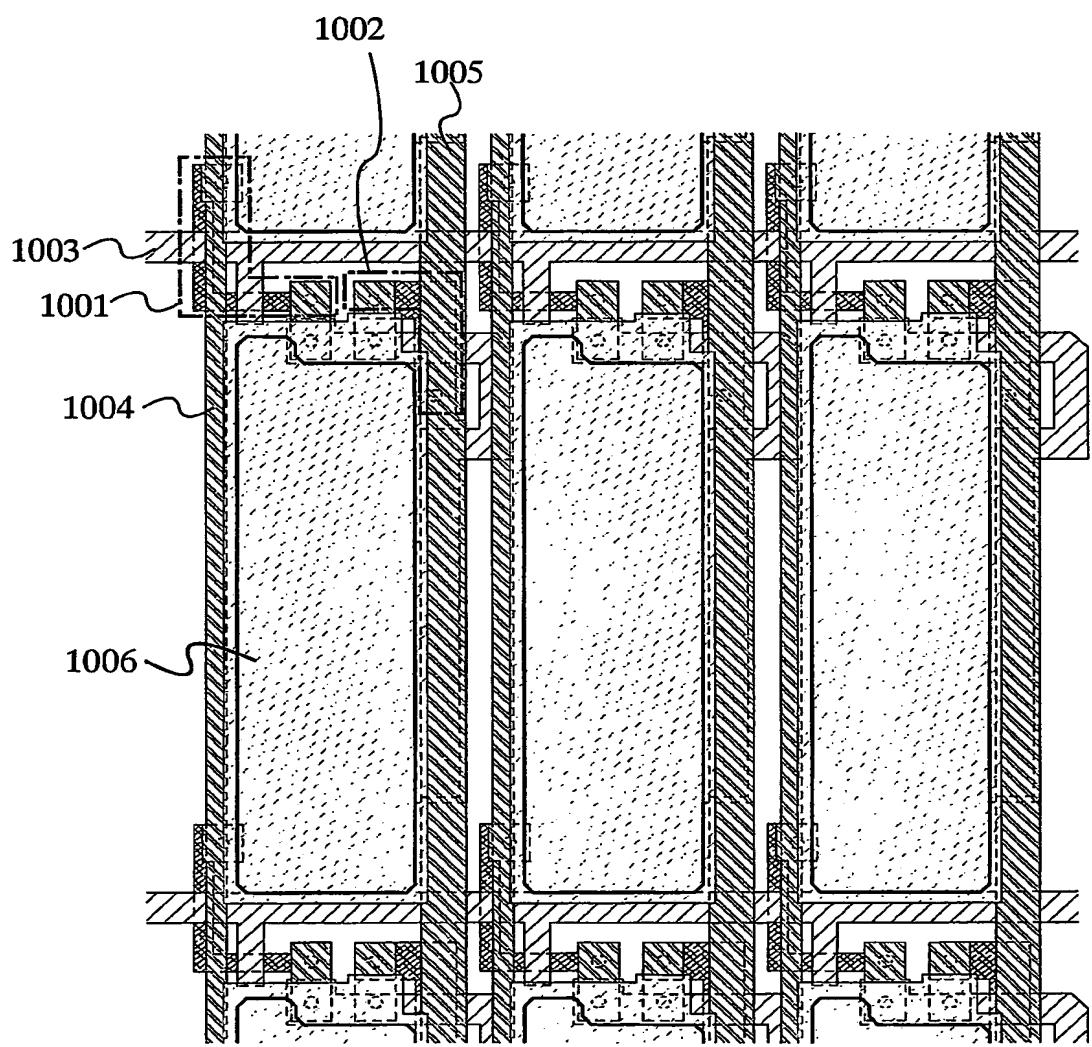
4/28

FIG. 4



5/28

FIG. 5



6/28

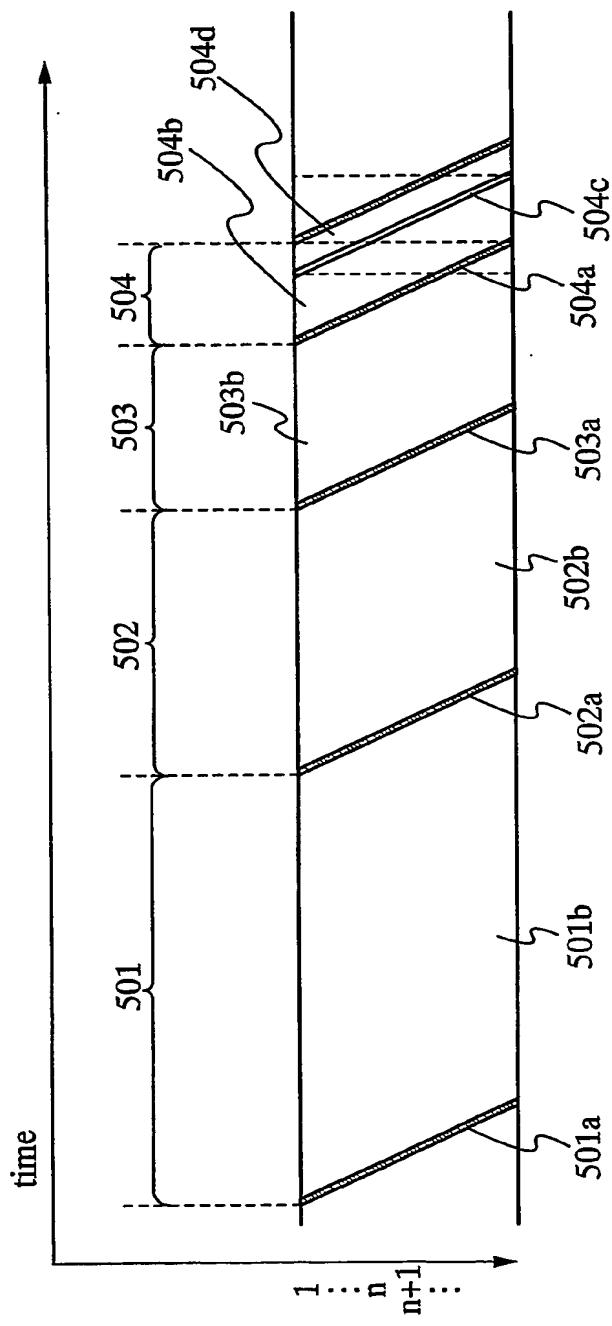
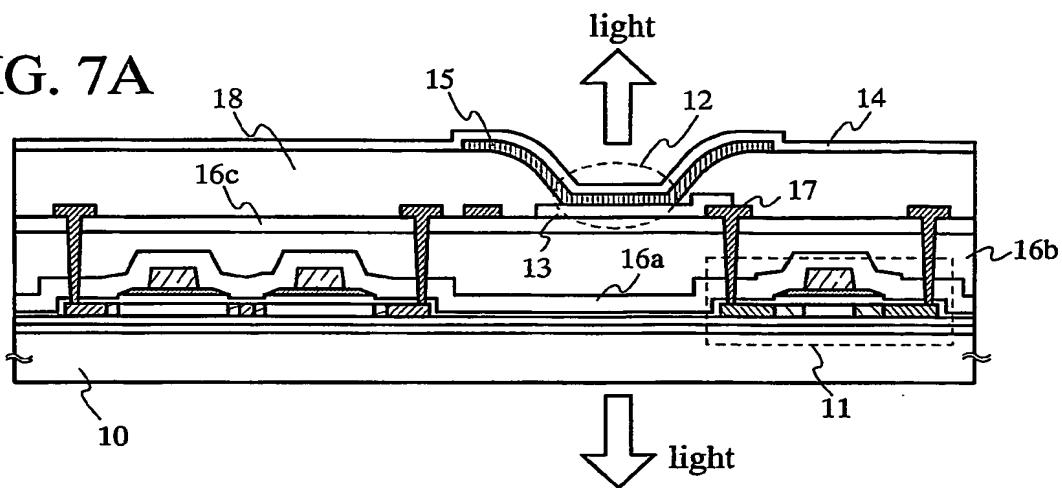
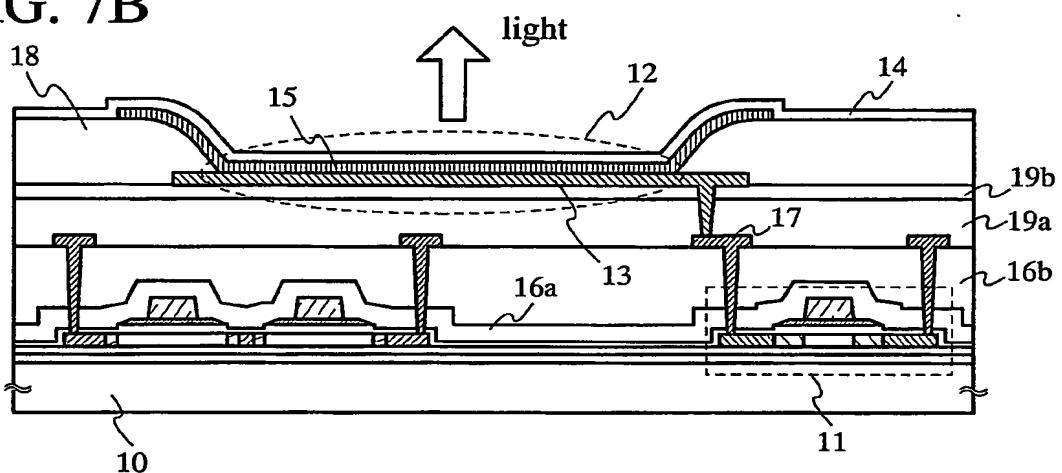
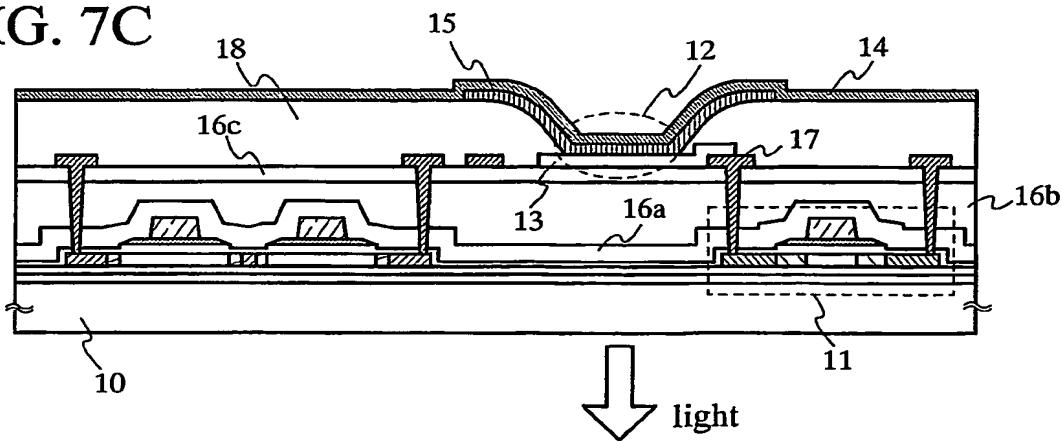
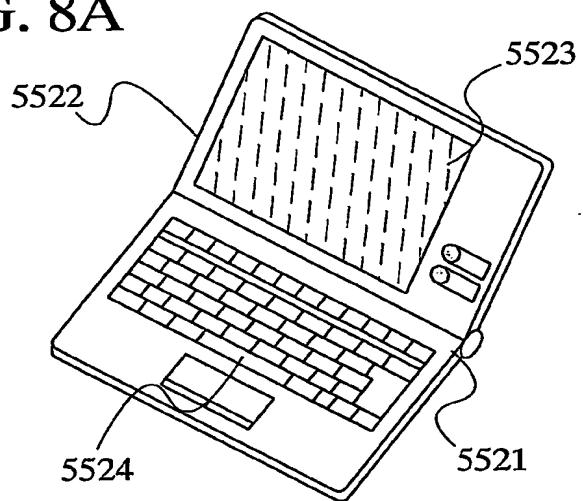
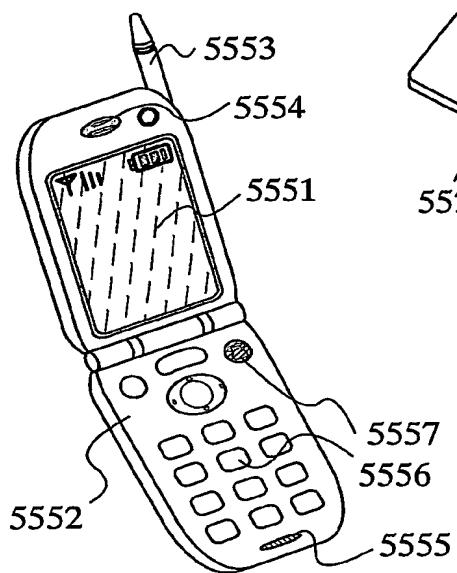
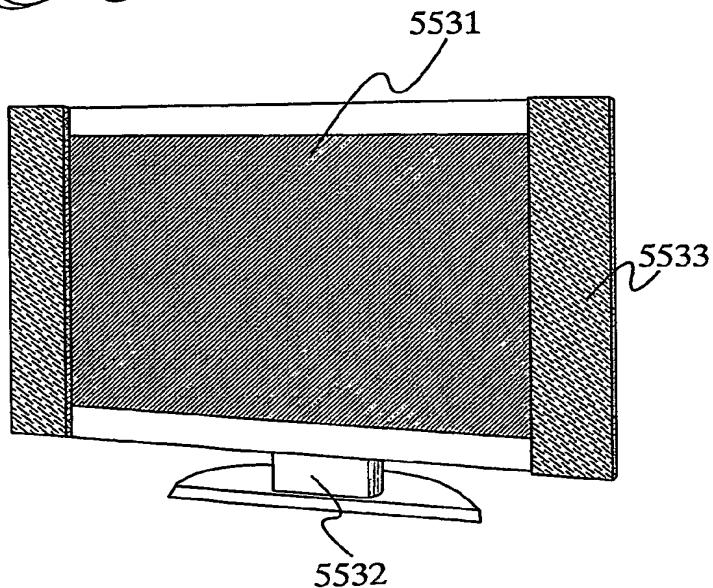


FIG. 6

7/28

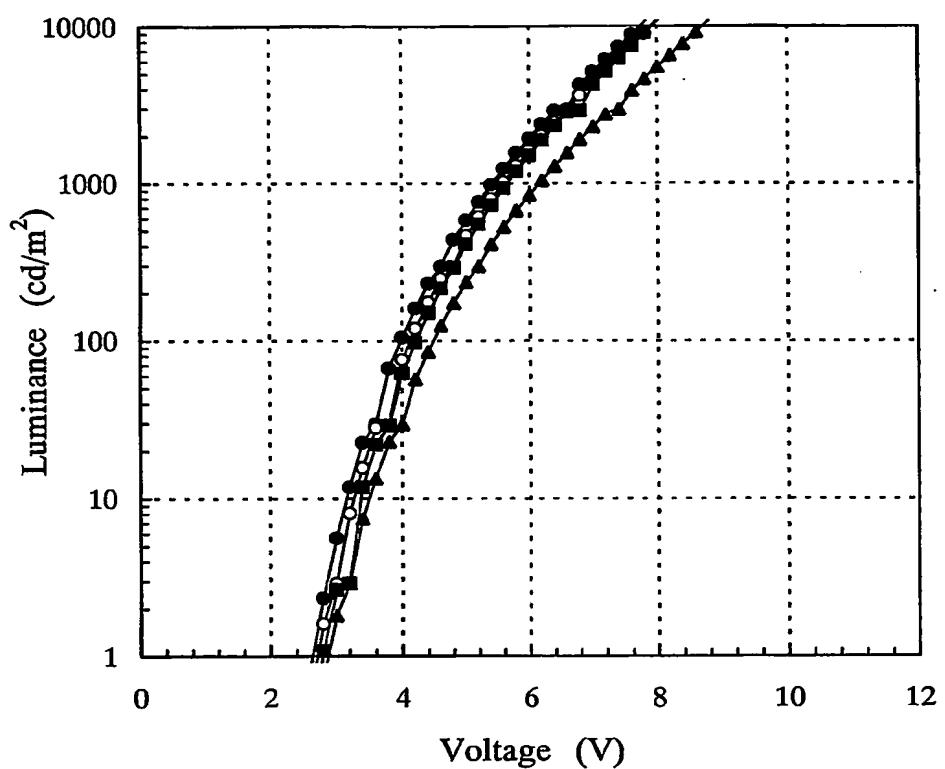
FIG. 7A**FIG. 7B****FIG. 7C**

8/28

FIG. 8A**FIG. 8B****FIG. 8C**

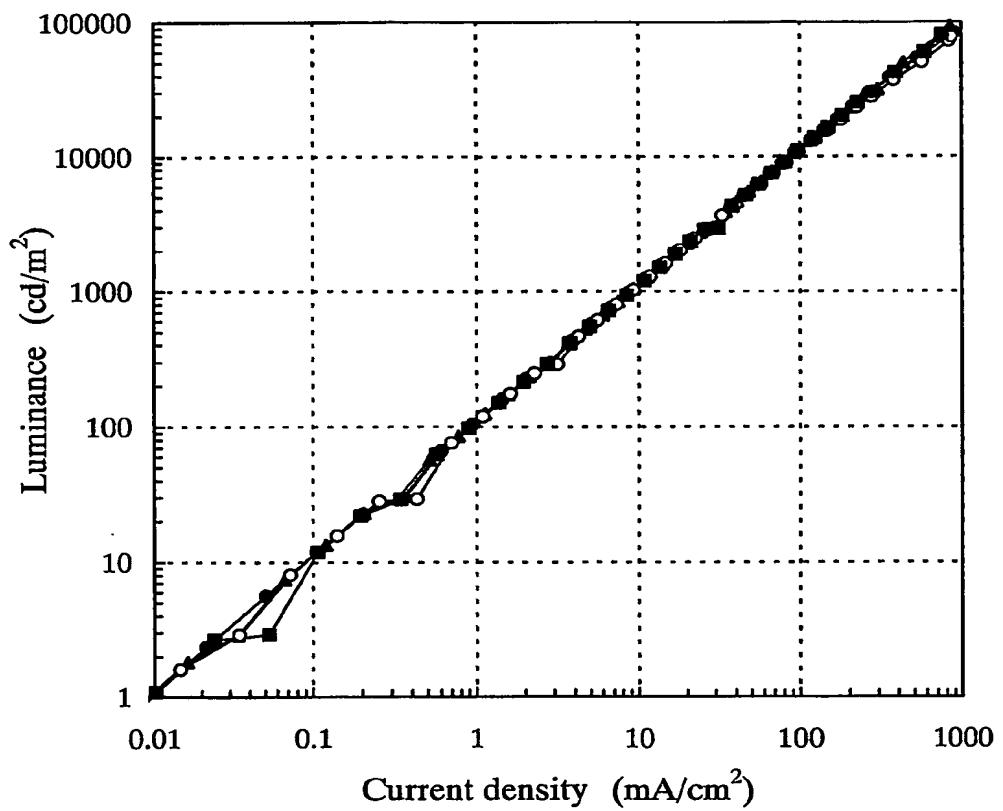
9/28

FIG. 9



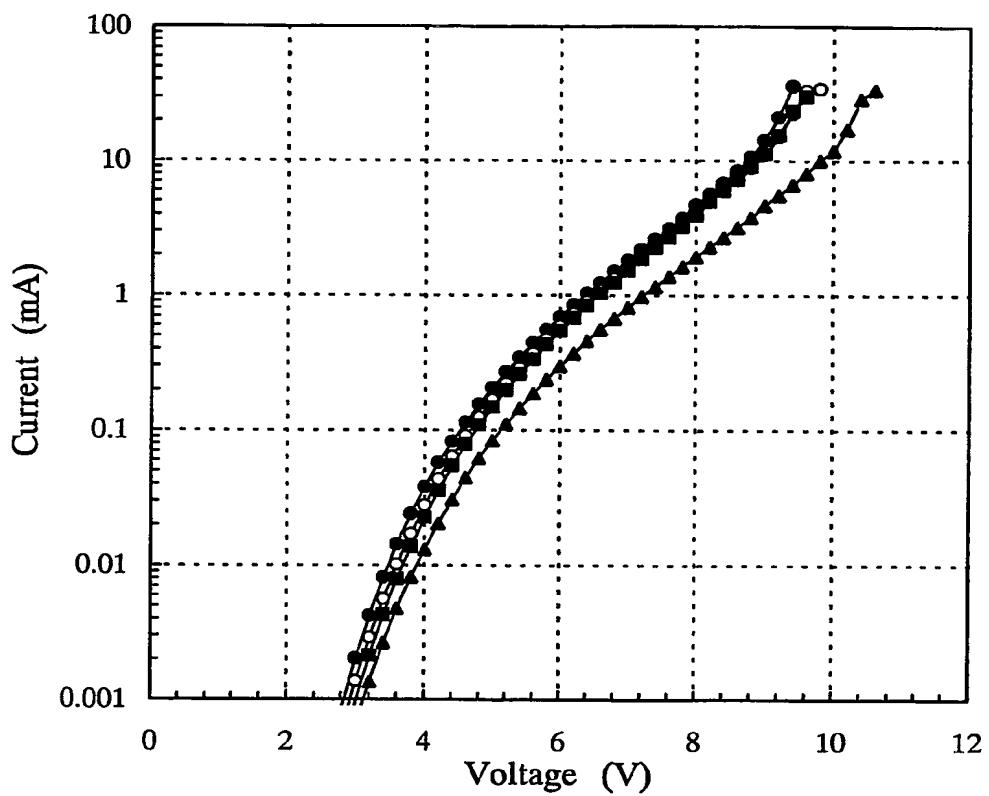
10/28

FIG. 10



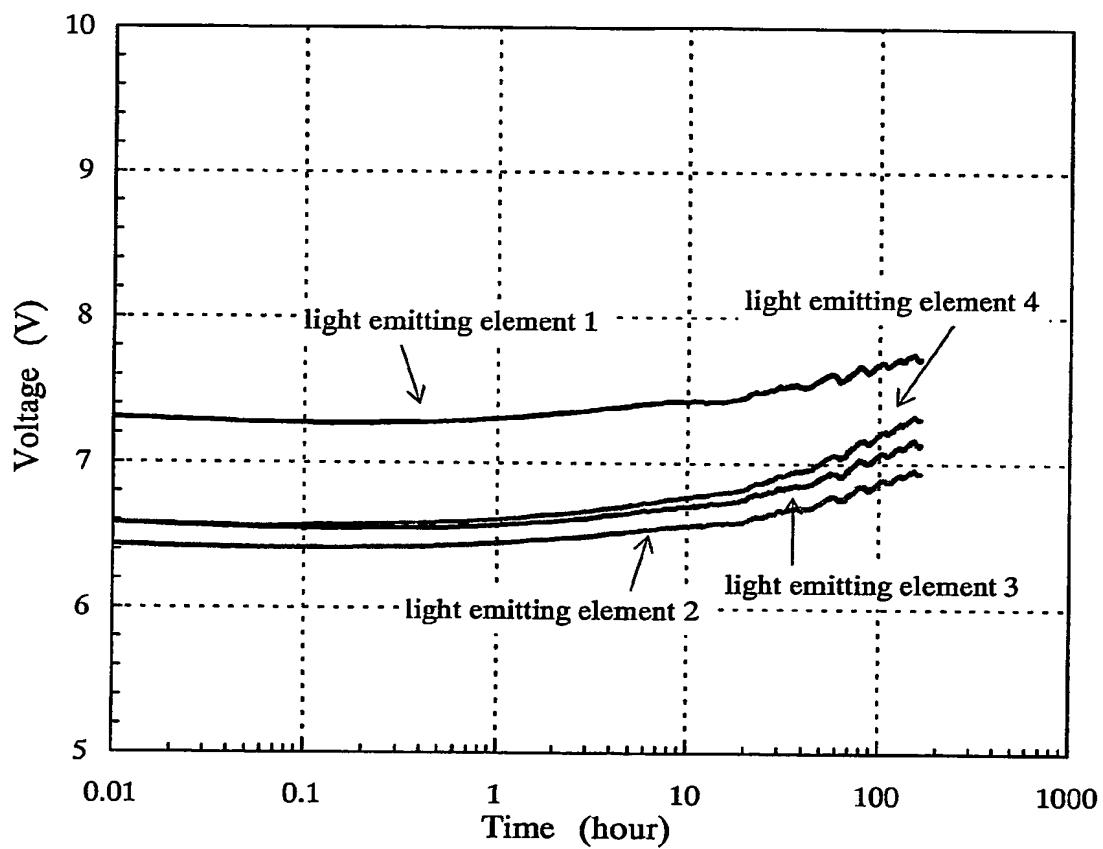
11/28

FIG. 11



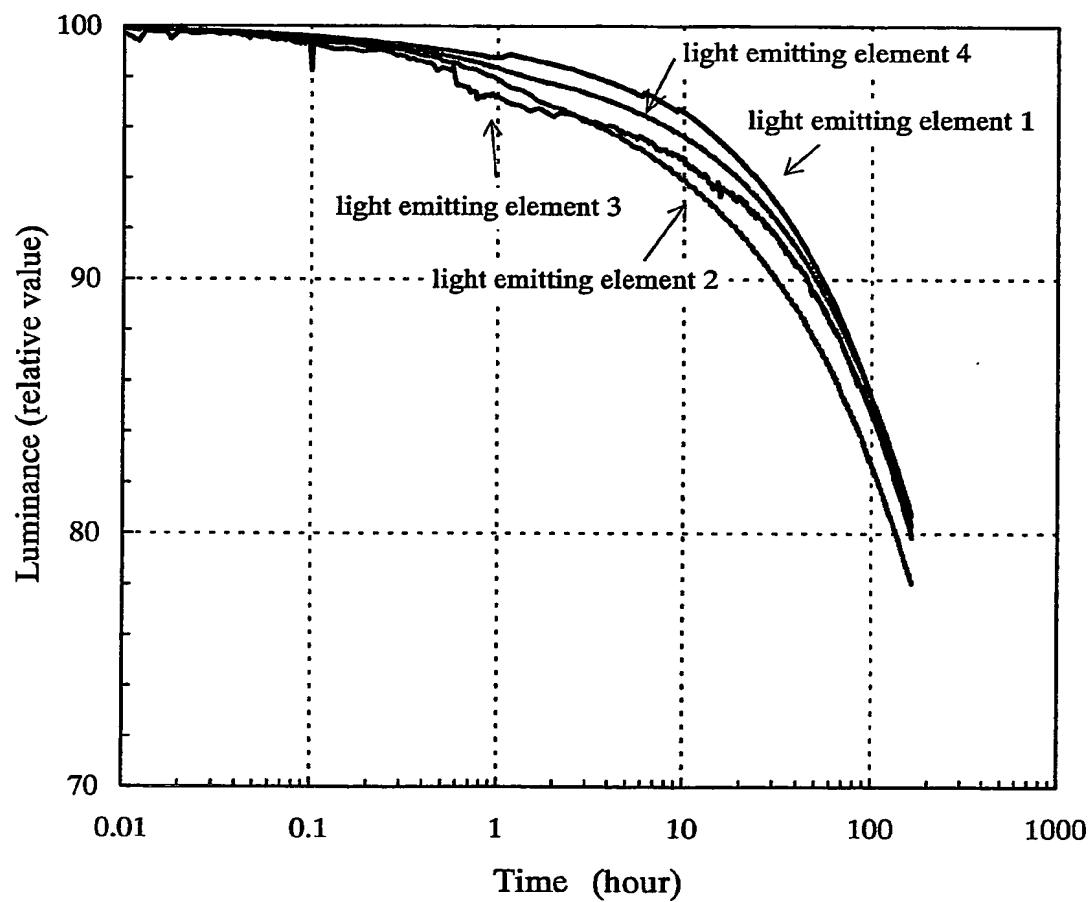
12/28

FIG. 12



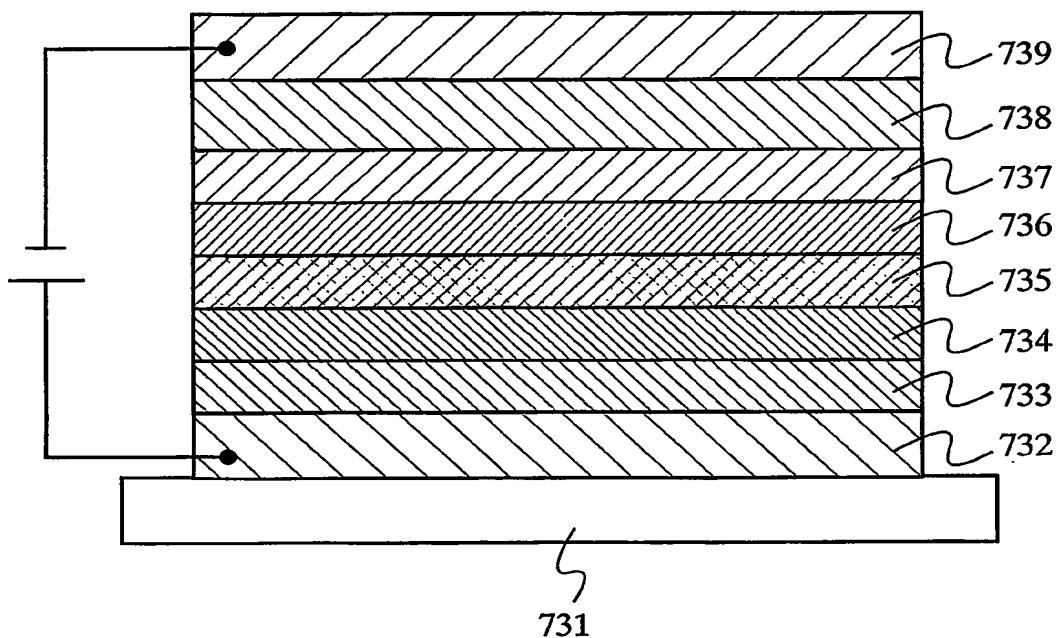
13/28

FIG. 13



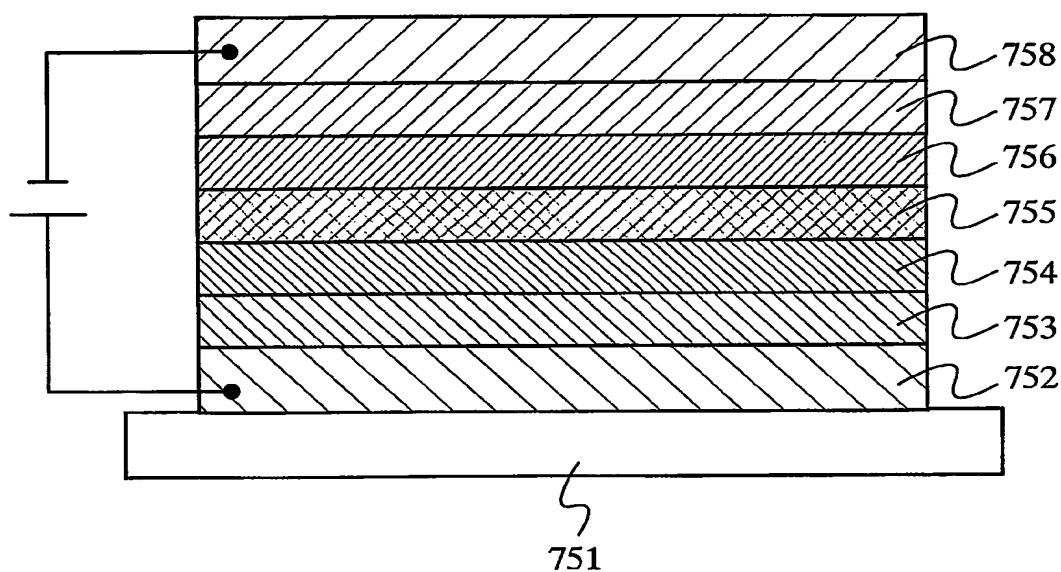
14/28

FIG. 14



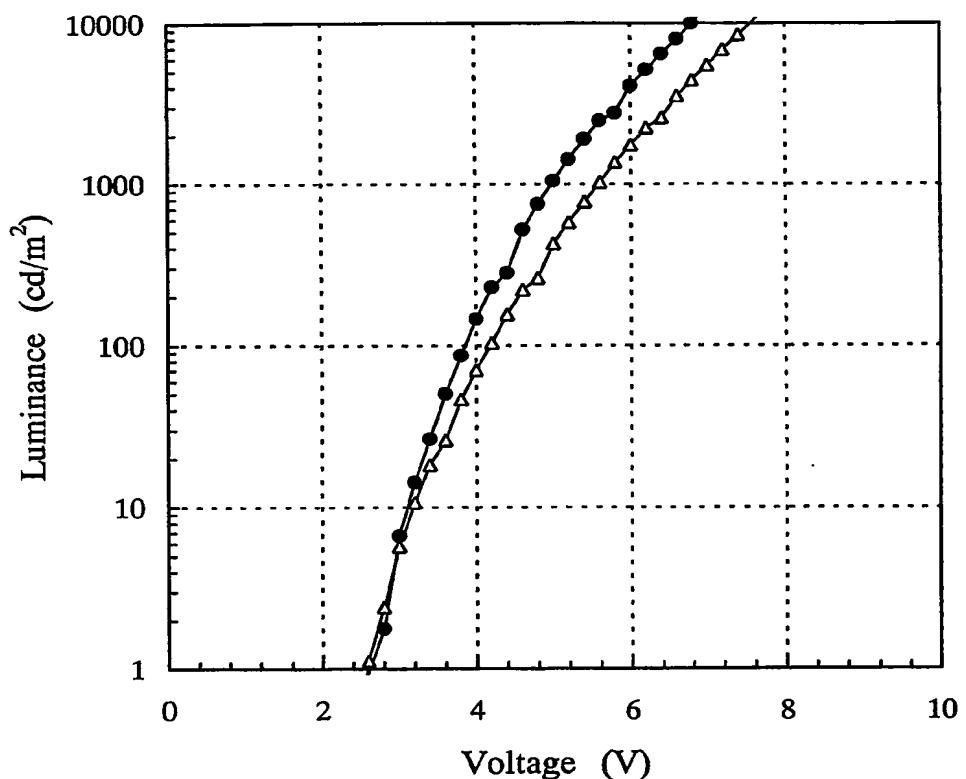
15/28

FIG. 15



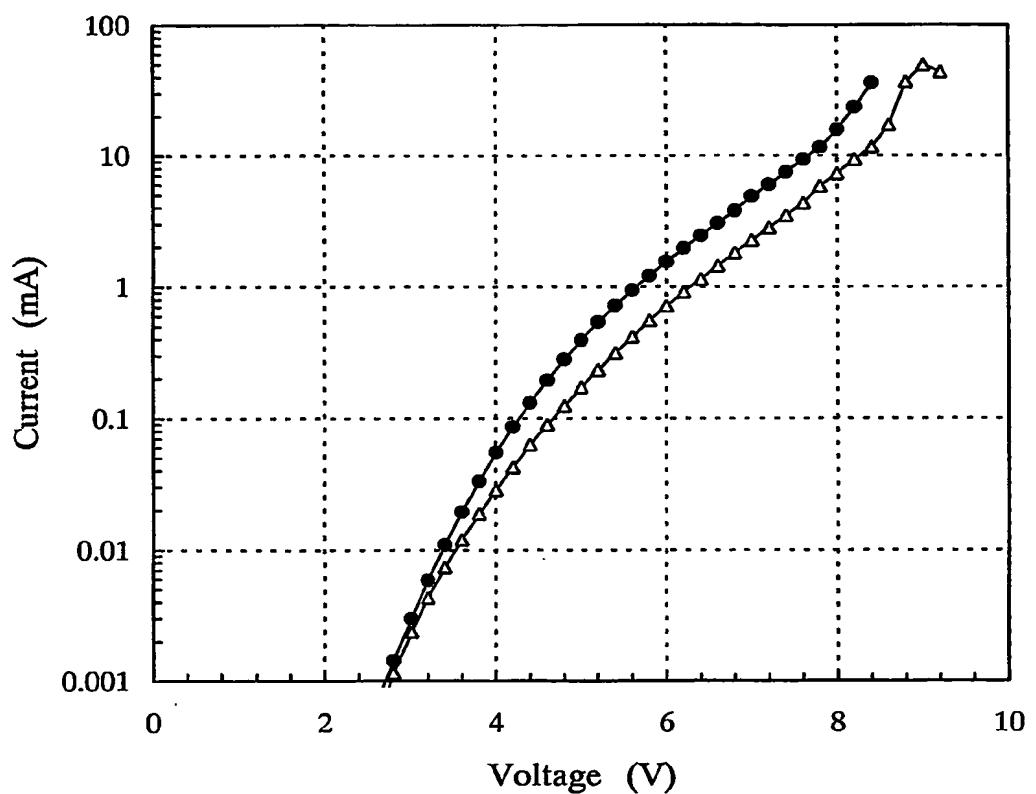
16/28

FIG. 16



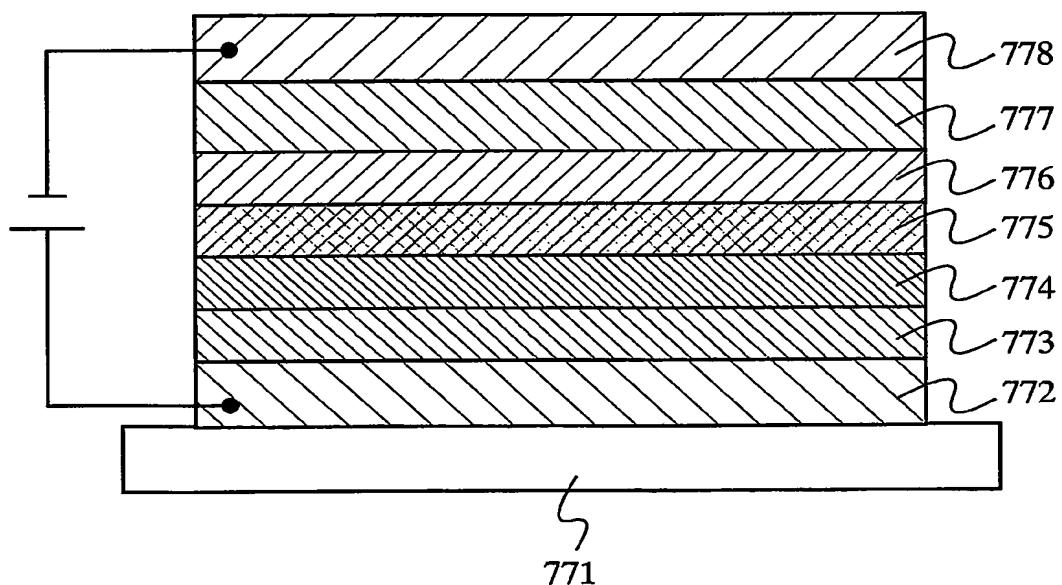
17/28

FIG. 17



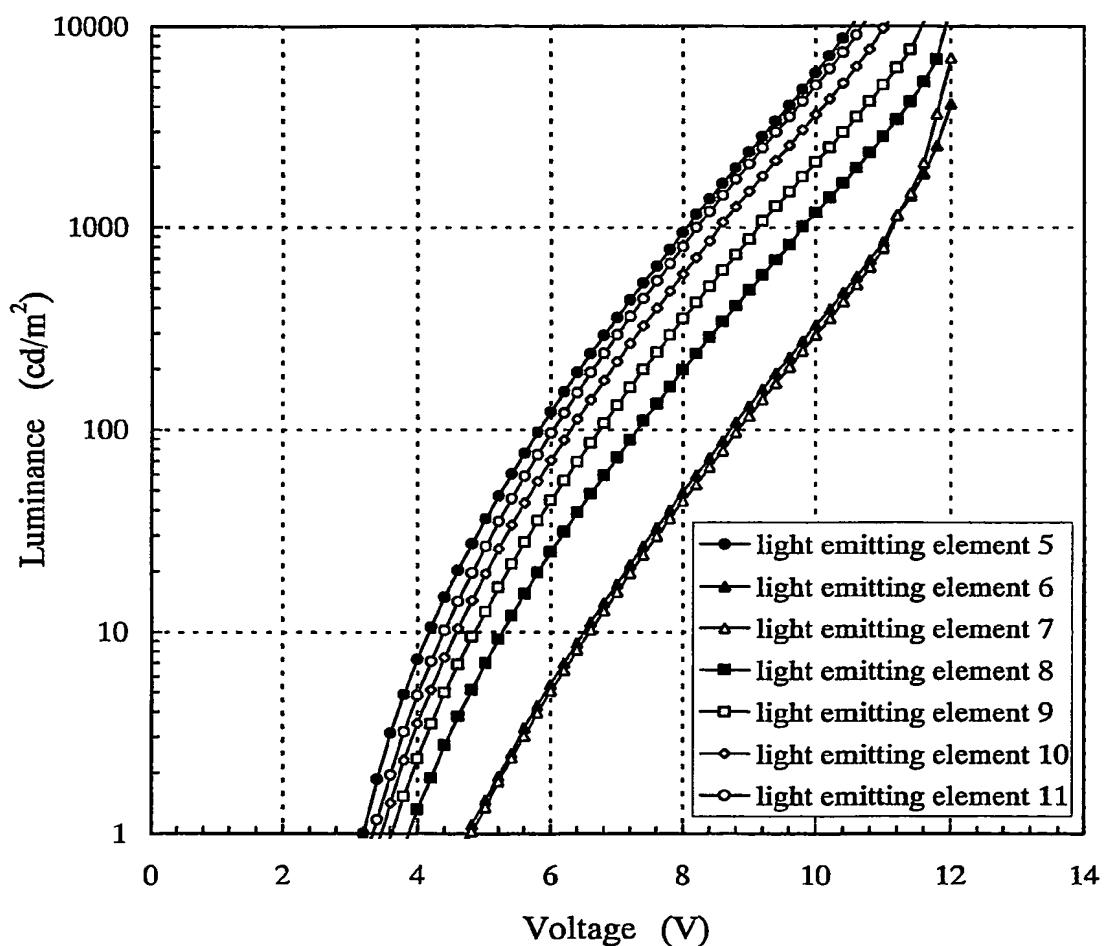
18/28

FIG. 18

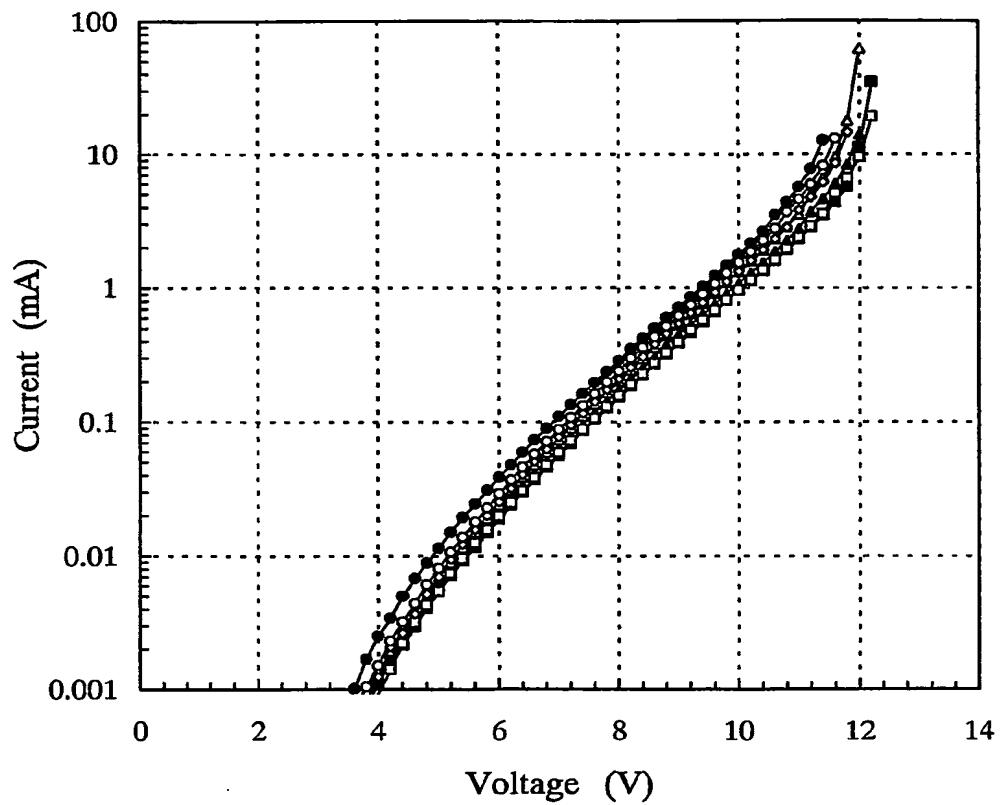


19/28

FIG. 19

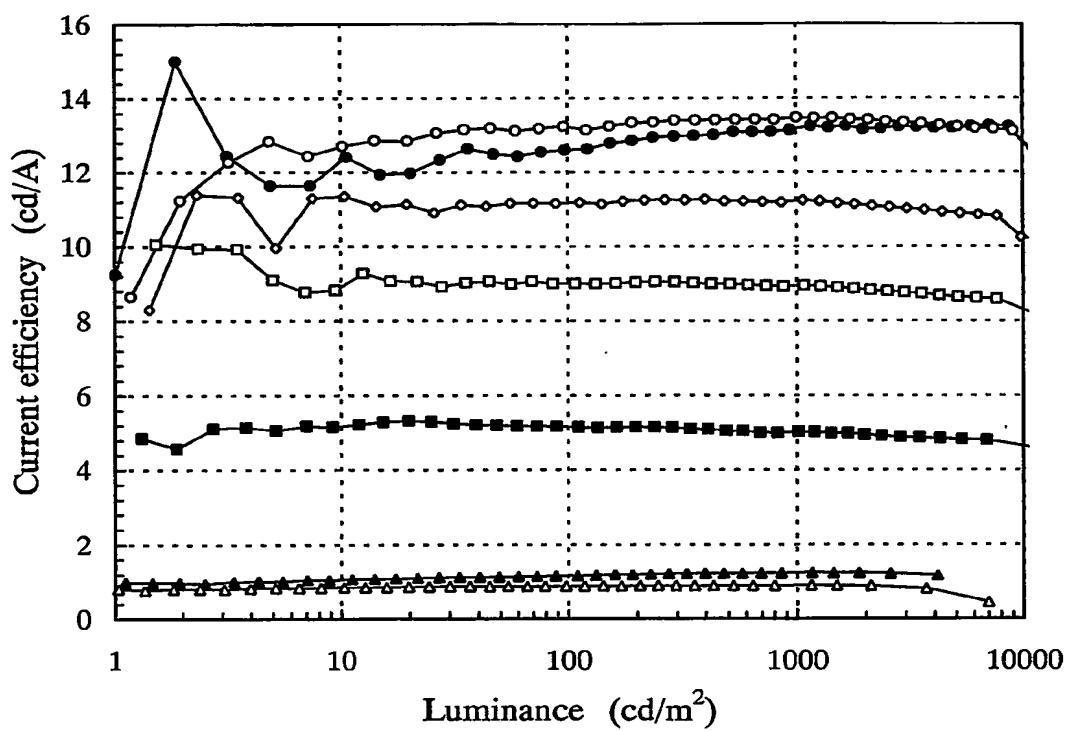


20/28

FIG. 20

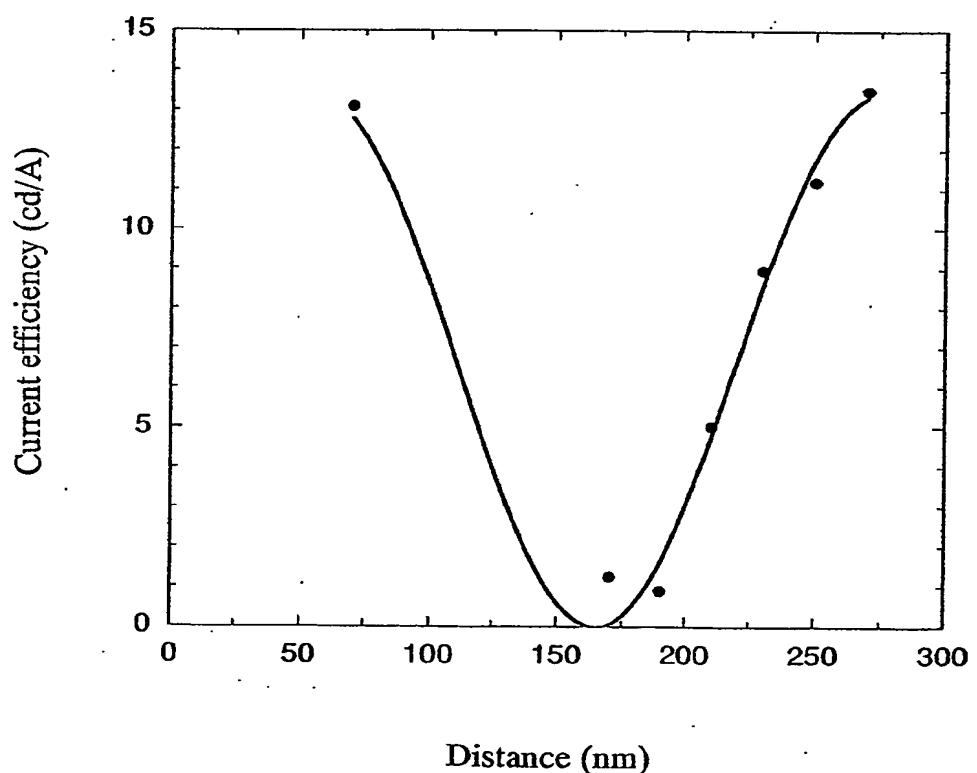
21/28

FIG. 21

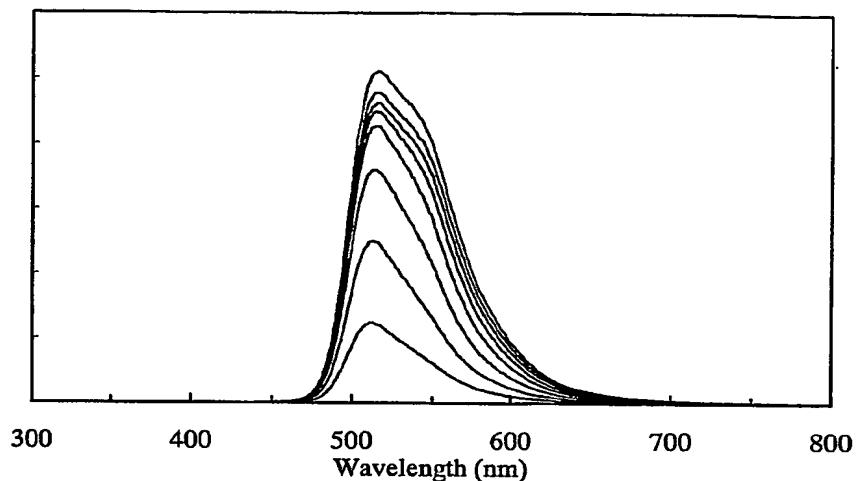
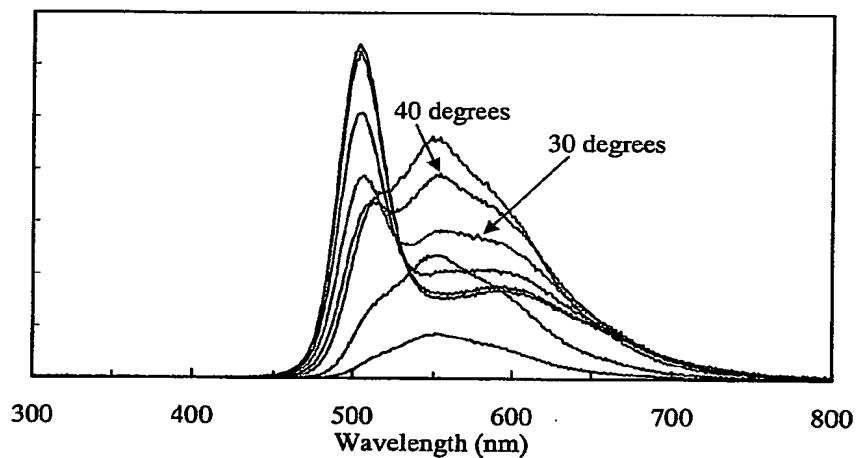
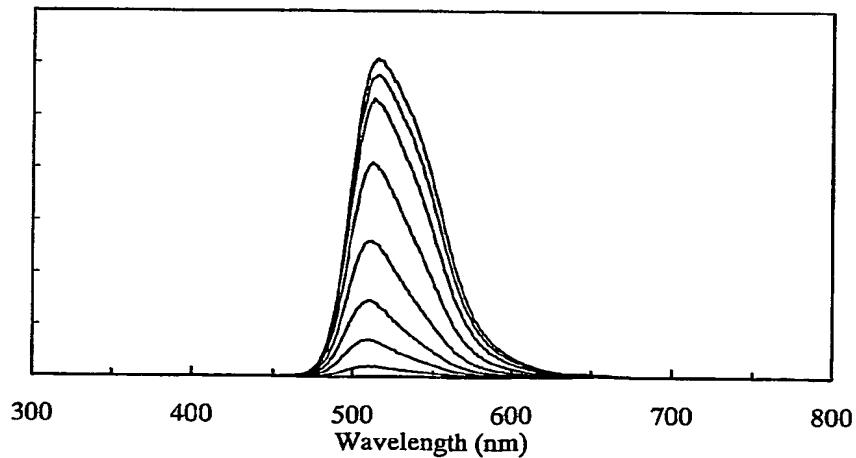


22/28

FIG. 22

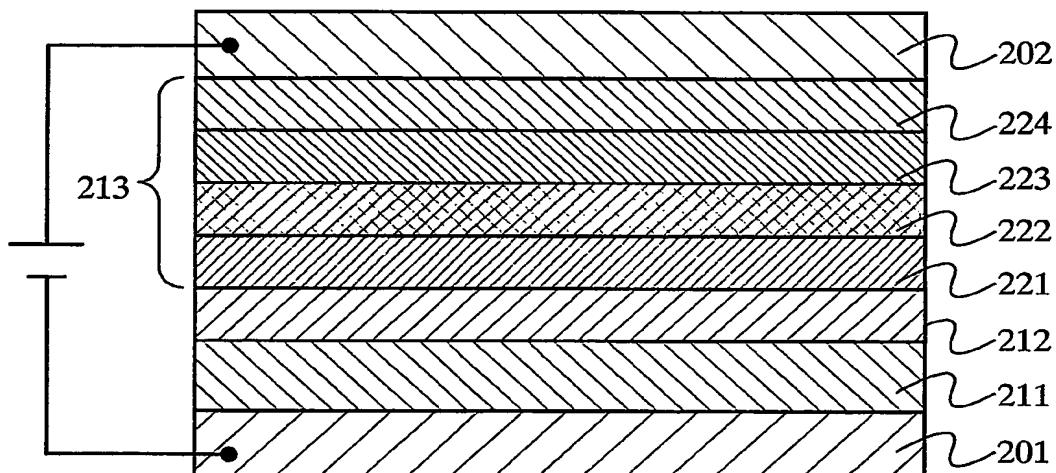


23/28

FIG. 23A**FIG. 23B****FIG. 23C**

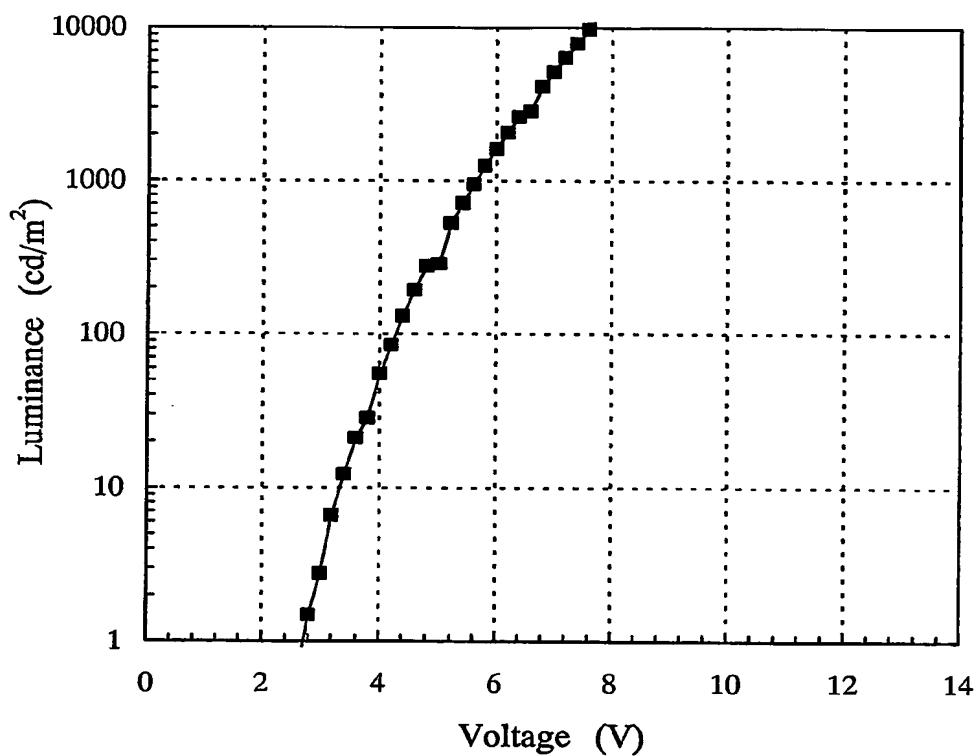
24/28

FIG. 24



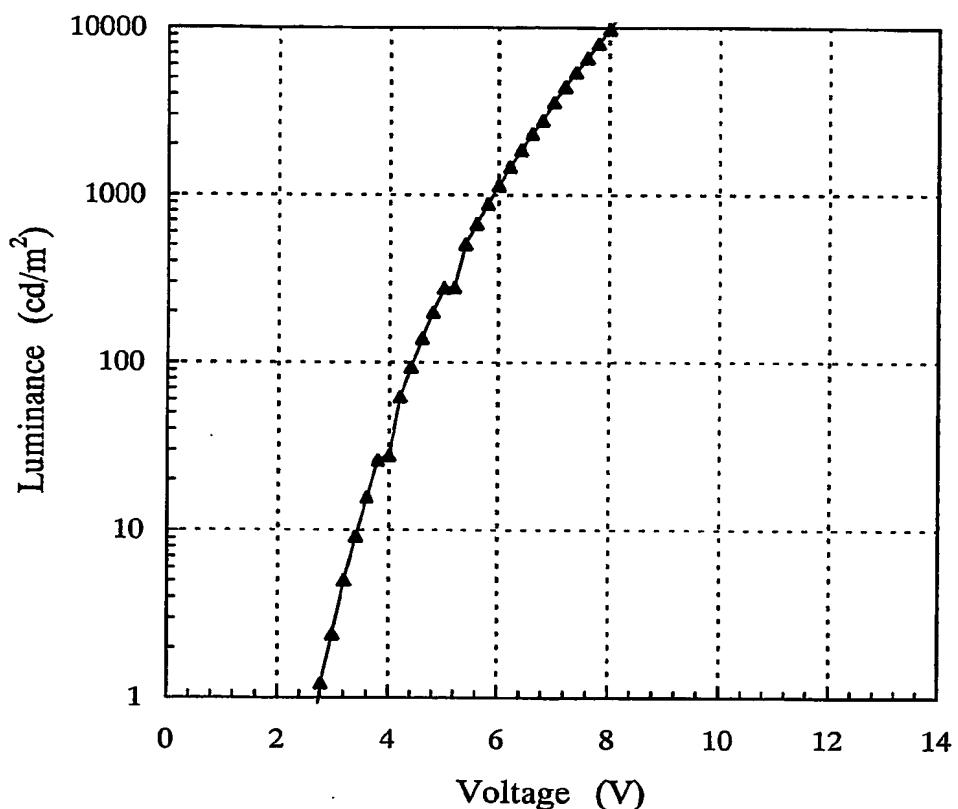
25/28

FIG. 25



26/28

FIG. 26



EXPLANATION OF REFERENCE

10: substrate, 11: transistor, 12: light emitting element, 13: first electrode, 14: second electrode, 15: layer, 16: first interlayer insulating film, 16a: interlayer insulating film, 16b: interlayer insulating film, 16c: interlayer insulating film, 17: wiring, 18: partition wall layer, 19: second interlayer insulating film, 19a: interlayer insulating film, 19b: interlayer insulating film, 101: first electrode, 102: second electrode, 111: first layer, 112: second layer, 113: third layer, 121: electron transporting layer, 122: light emitting layer, 123: hole transporting layer, 124: hole injecting layer, 201: first electrode, 202: second electrode, 211: first layer, 212: second layer, 213: third layer, 221: electron transporting layer, 222: light emitting layer, 223: hole transporting layer, 224: hole injecting layer, 501: sub-frame, 501a: writing period, 501b: holding period, 502: sub-frame, 502a: writing period, 502b: holding period, 503: sub-frame, 503a: writing period, 503b: holding period, 504: sub-frame, 504a: writing period, 504b: holding period, 504c: erasing period, 504d: non light emitting period, 701: substrate, 702: second electrode, 703: layer, 704: layer, 705: layer, 706: layer, 707: second layer, 708: first layer, 709: first electrode, 731: substrate, 732: second electrode, 733: layer, 734: layer, 735: layer, 736: layer, 737: second layer, 738: first layer, 739: first electrode, 751: substrate, 752: second electrode, 753: layer, 754: layer, 755: layer, 756: layer, 757: second layer, 758: first electrode, 771: substrate, 772: second electrode, 773: layer, 774: layer, 775: layer, 776: second layer, 777: first layer, and 778: first electrode, 901: first transistor, 902: second transistor, 903: light emitting element, 911: gate signal line, 912: source signal line, 913: writing gate signal line driver circuit, 914: erasing gate signal

28/28

line driver circuit, 915: source signal line driver circuit, 916: power source, 917: current supply line, 918: switch, 919: switch, 920: switch, 1001: first transistor, 1002: second transistor, 1003: gate signal line, 1004: source signal line, 1005: current supply line, 1006: electrode, 5521: main body, 5522: housing, 5523: display portion, 5524: keyboard, 5531: display portion, 5532: housing, 5533: speaker, 5551: display portion, 5552: main body, 5553: antenna, 5554: audio output portion, 5555: audio input portion, 5556: operation switch, 5557: operation switch, 6500: substrate, 6503: flexible printed circuit (FPC), 6504: printed wiring board (PWB), 6511: pixel portion, 6512: source signal line driver circuit, 6513: writing gate signal line driver circuit, 6514: erasing gate signal line driver circuit